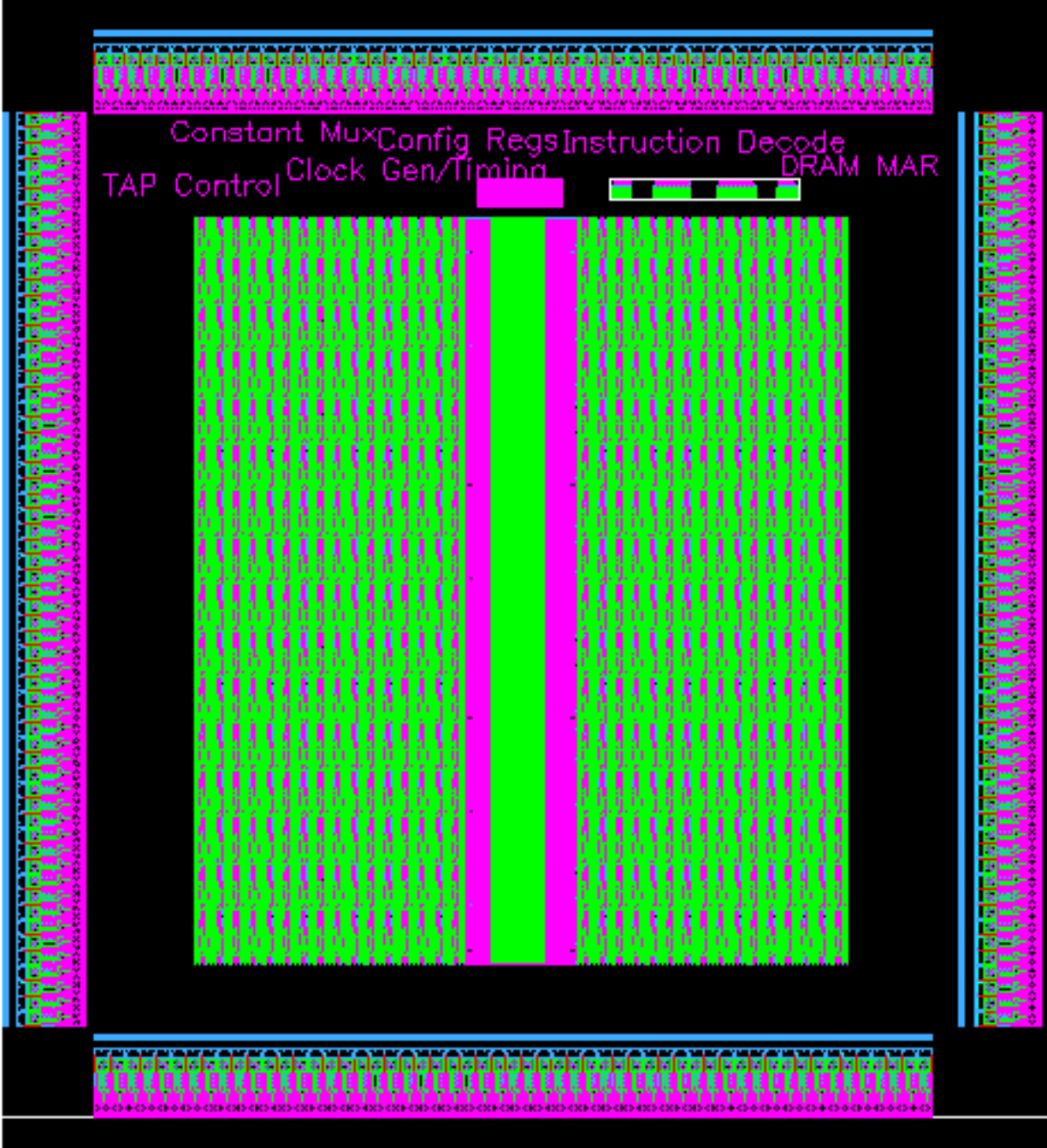
Constant Mux
Config Regs
Instruction Decode
TAP Control
Clock Gen/Timing
DRAM MAR