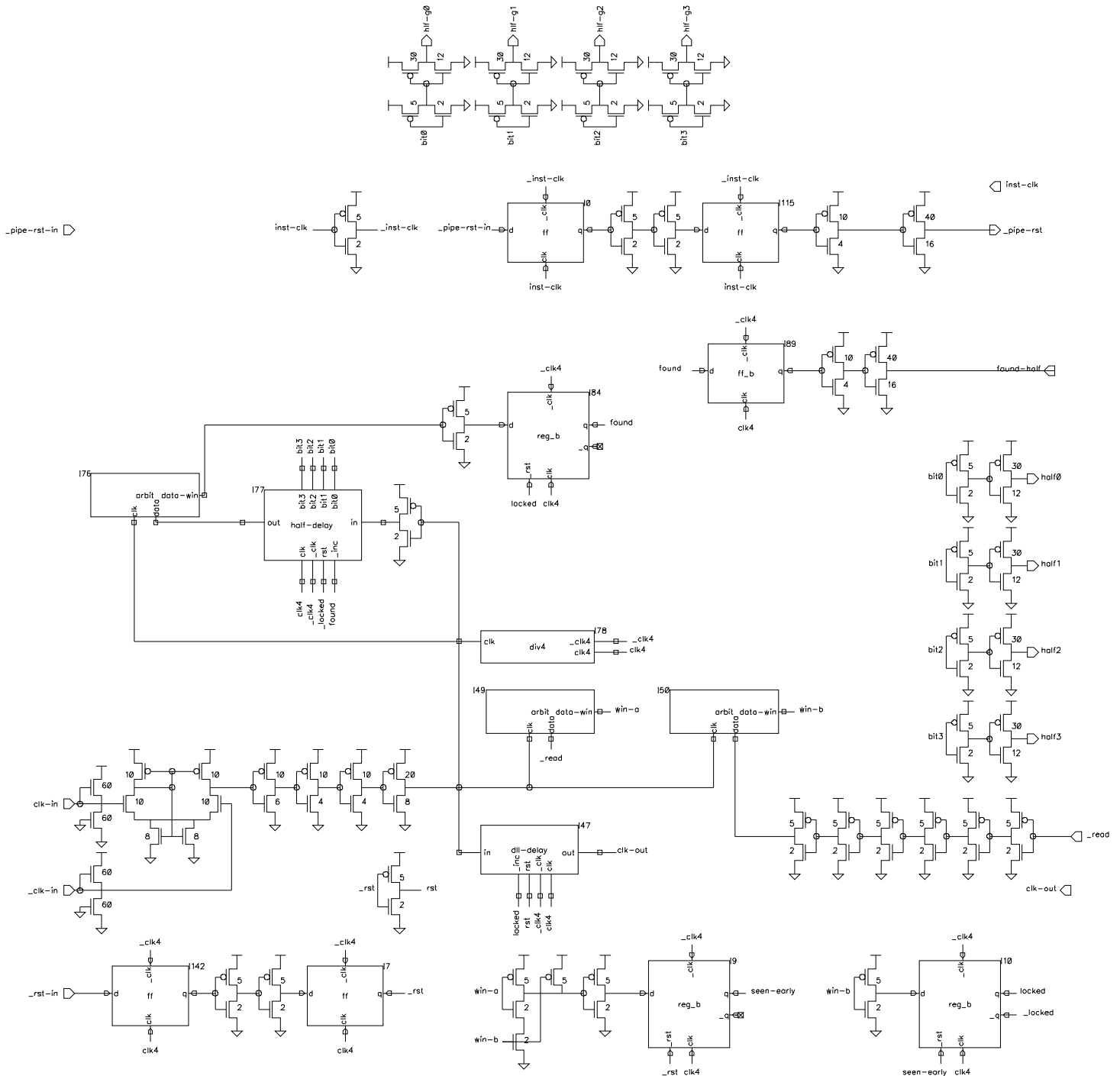
hif_g0
hif_g1
hif_g2
hif_g3
bit0
bit1
bit2
bit3
_pipe_rst_in
inst_clk
_inst_clk
ff
_pipe_rst
reg_b
found
locked
clk4
ff_b
found_half
arbit_data_win
half_delay
div4
dll_delay
clk_out
win_a
win_b
seen_early
_read
_rst_in
_rst
clk_in
half0
half1
half2
half3