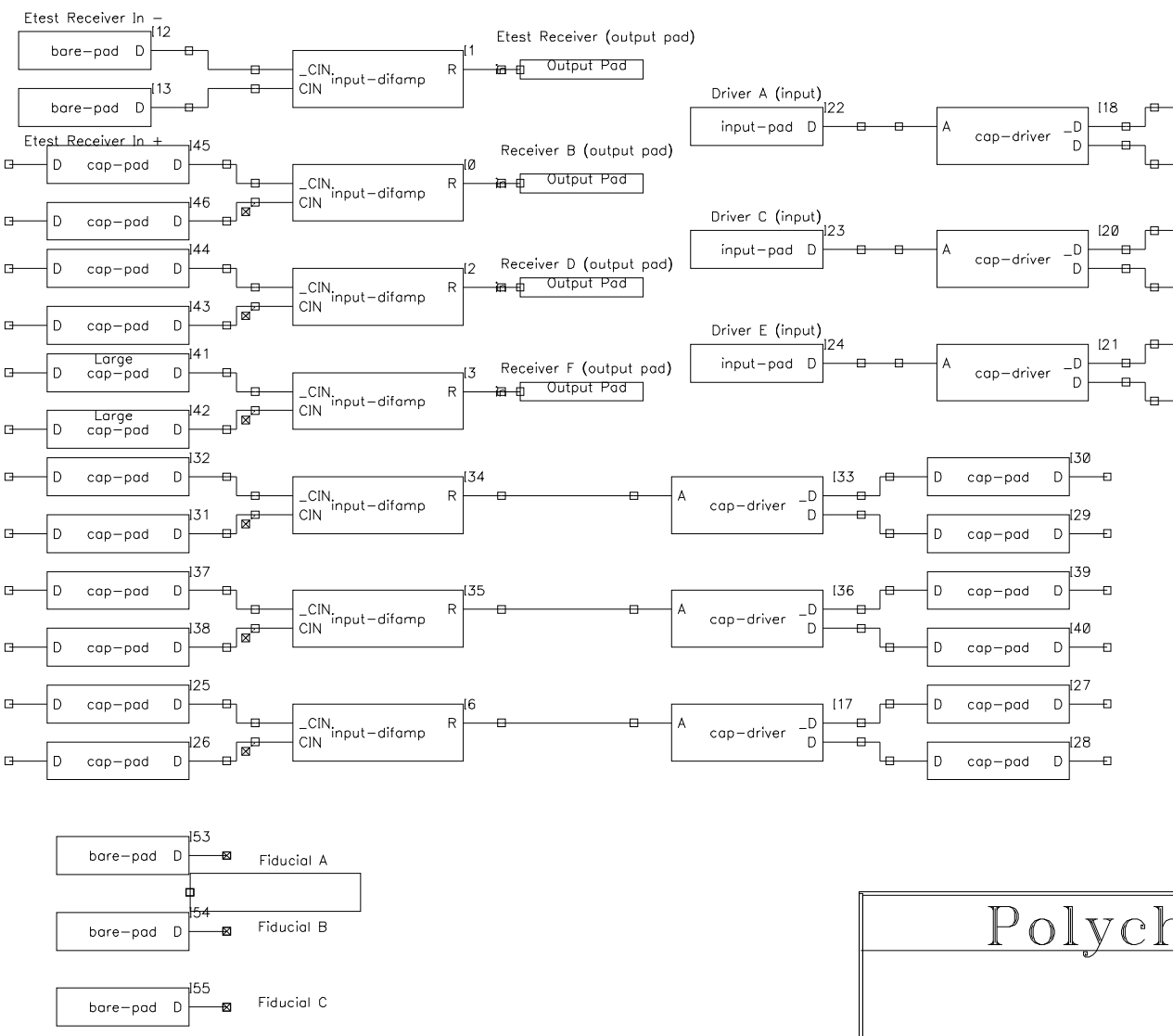
CAGE NO.
DWG NO.
tk2/toplevel
Etest Receiver In −
bare−pad D
I12
bare−pad D
I13
_CIN
CIN
input−difamp
R
I1
Etest Receiver (output pad)
Output Pad
Etest Receiver In +
D cap−pad D
I45
D cap−pad D
I46
D cap−pad D
I44
D cap−pad D
I43
D Large cap−pad D
I41
D Large cap−pad D
I42
I0
Receiver B (output pad)
I2
Receiver D (output pad)
I3
Receiver F (output pad)
D cap−pad D
I32
D cap−pad D
I31
D cap−pad D
I37
D cap−pad D
I38
D cap−pad D
I25
D cap−pad D
I26
I34
I35
I6
Driver A (input)
input−pad D
I22
Driver C (input)
I23
Driver E (input)
I24
A
cap−driver
_D
D
I18
I20
I21
I33
I36
I17
D cap−pad D
I30
D cap−pad D
I29
I39
I40
I27
I28
bare−pad D
I53
Fiducial A
I54
Fiducial B
I55
Fiducial C
Polych
DRAWN BY
DATE
Nov 15 15:2