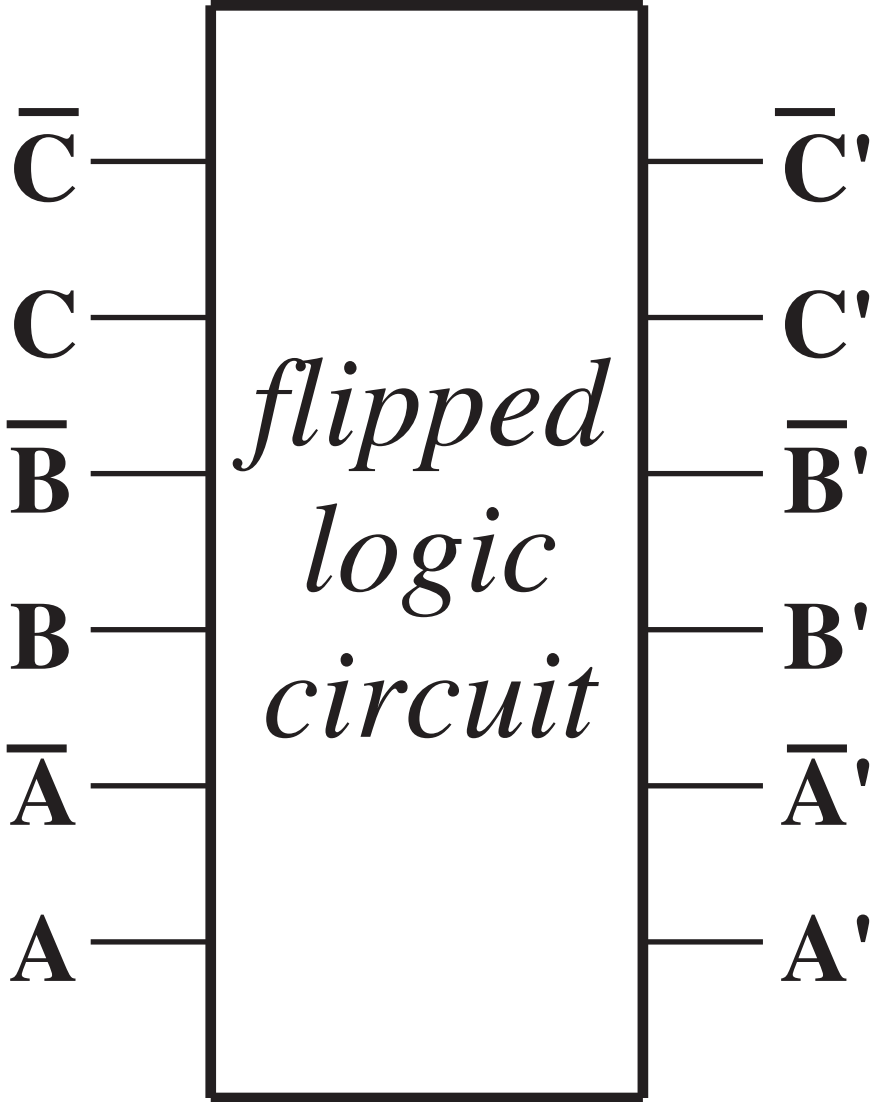
$\overline{C}$
C
$\overline{B}$
B
$\overline{A}$
A
flipped logic circuit
$\overline{C}'$
C'
$\overline{B}'$
B'
$\overline{A}'$
A'